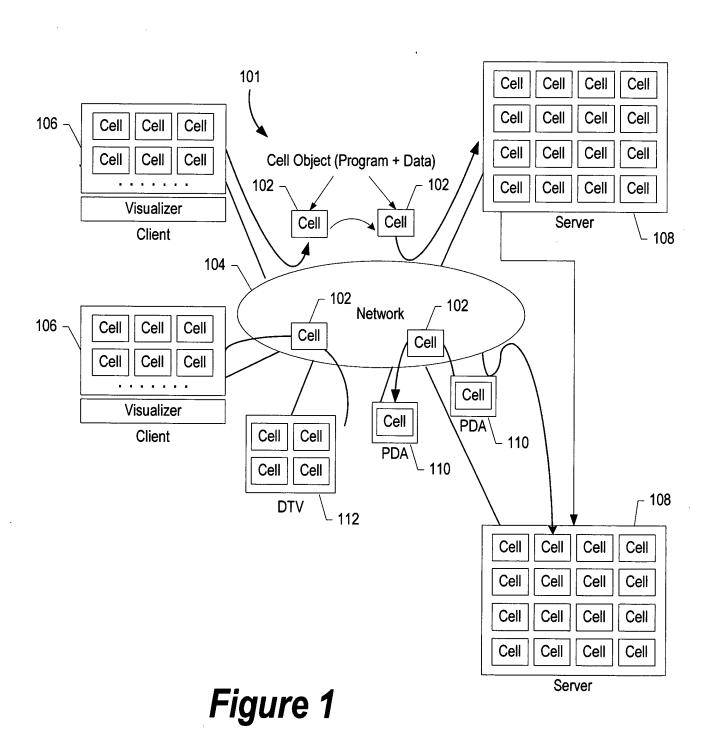
D cket No. AUS920030790US1

Minor, tal.

Syst m and Method f r Balancing Computati nal L ad Acr ss a Plurality of Processors



D cket N . AUS920030790US1

Min r, et al.

System and M th df r Balancing Computati nal L ad Acr ss a Plurality f Pr c ss rs

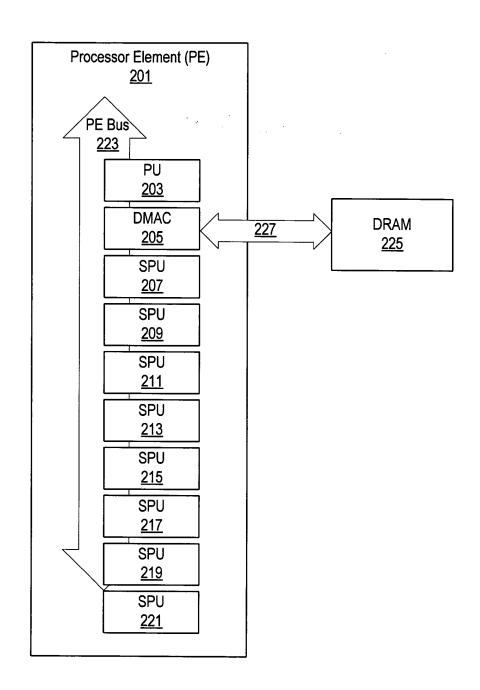


Figure 2

D cket N . AUS920030790US1

Min r, et al.

Syst m and Meth d f r Balancing C mputati nal L ad Across a Plurality of Pr cess rs

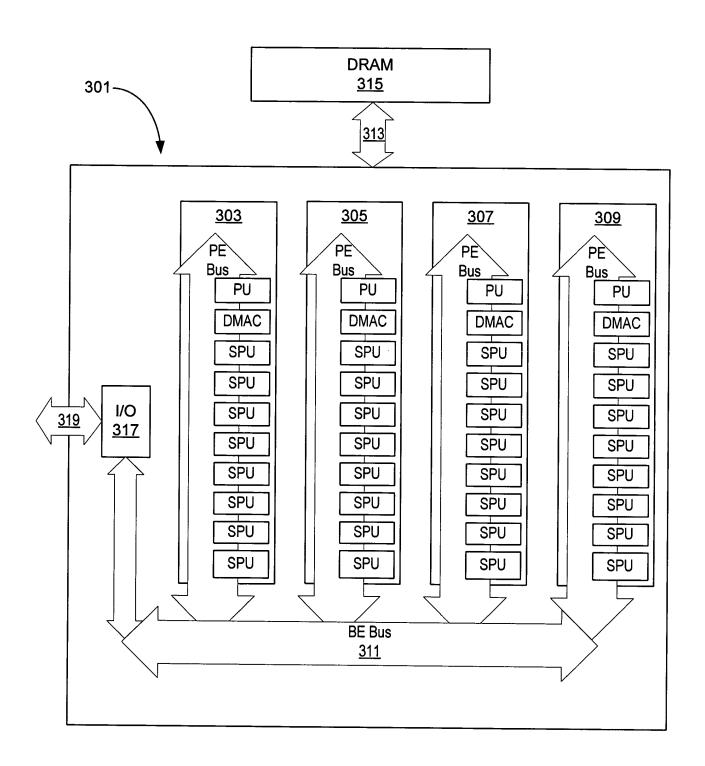


Figure 3

Docket N.AUS920030790US1

Min r, et al.

System and M th d for Balancing C mputational Load Acr ss a Plurality f Pr c ss rs

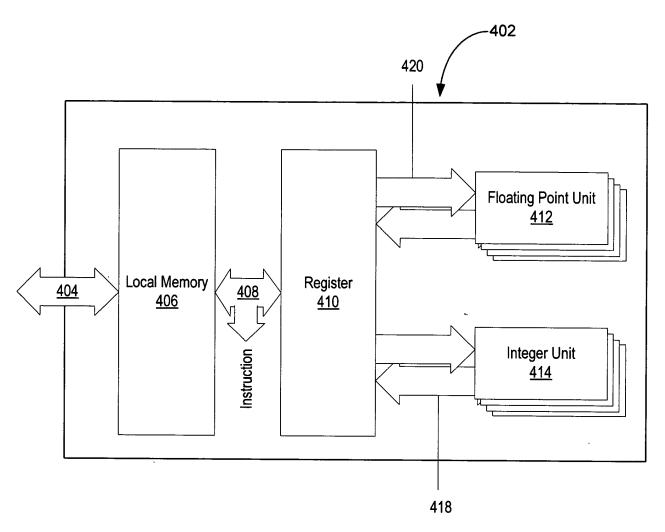


Figure 4

Dock tN . AUS920030790US1 Min r, tal.

System and Meth df r Balancing Computati nal Load Acr ss a Plurality of Pr c ssors

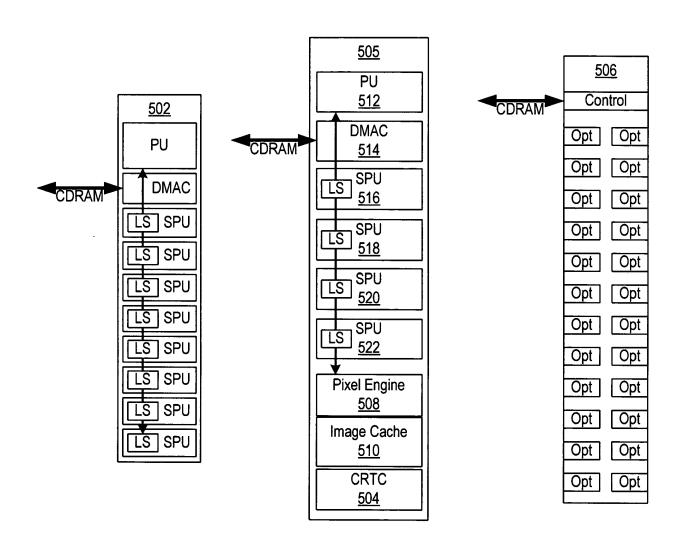
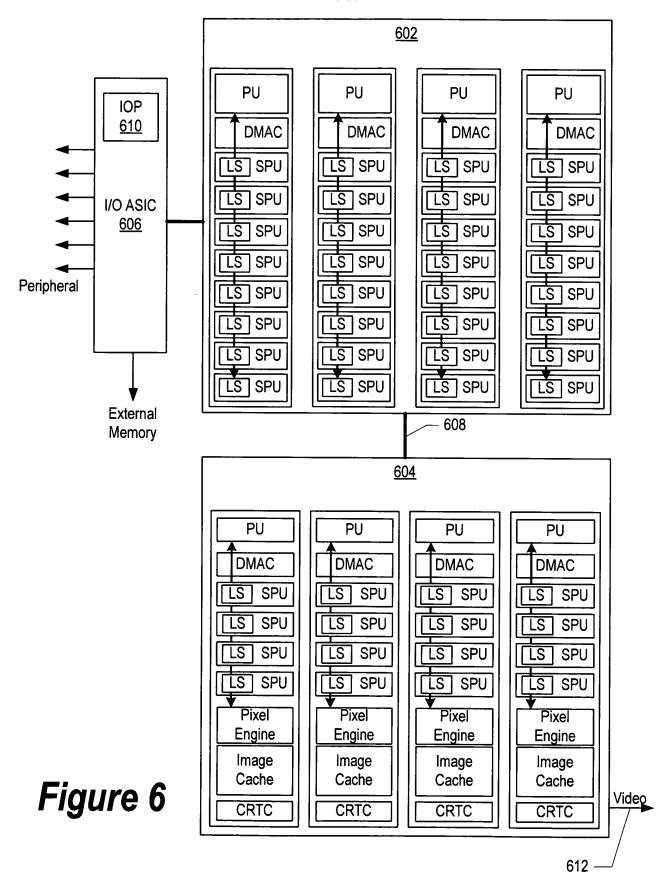


Figure 5

D cket No. AUS920030790US1 Minor, et al. Syst m and M th d f r Balancing C mputati nal L ad Across a Plurality of Proc ssors



D ck t No. AUS920030790US1 Min r, et al.

System and M th df r Balancing Computati nal Load Acr ss a Plurality f Pr c ss rs

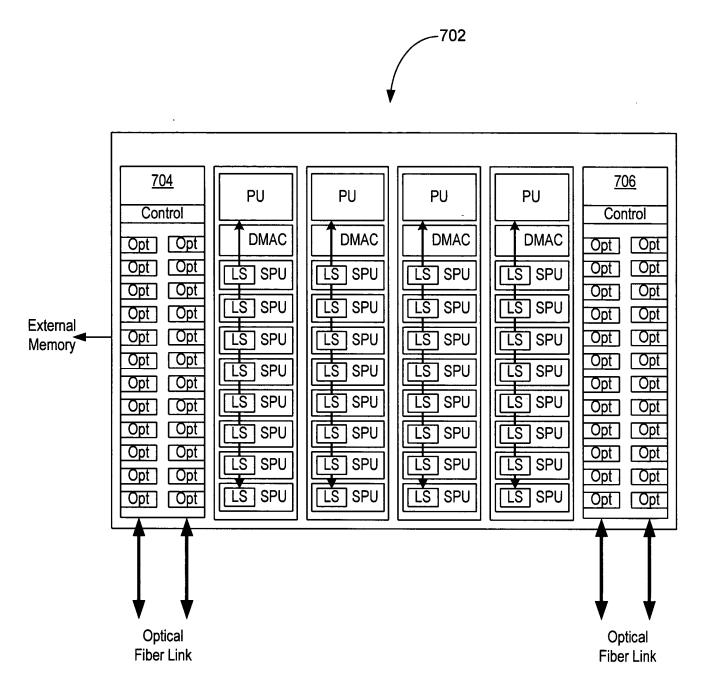


Figure 7

D ck t No. AUS920030790US1

Min r, et al.

Syst m and Meth d for Balancing C mputational Load Acr ss a Plurality f Pr c ss rs

8 / 51

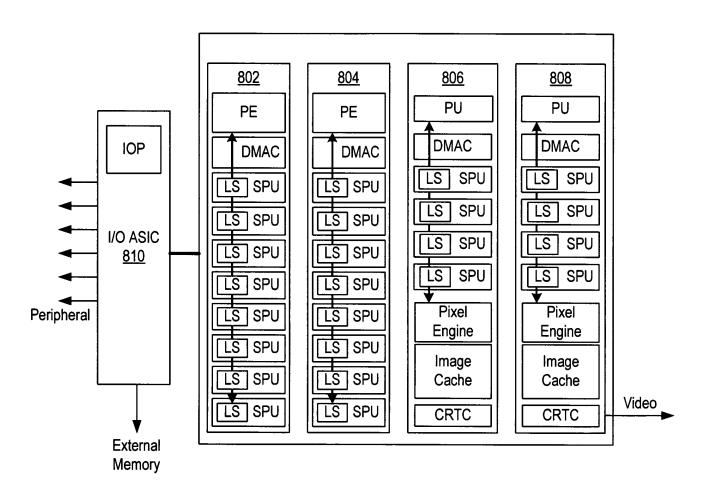


Figure 8

Docket No. AUS920030790US1

Minor, tal.

System and M th d for Balancing Computational L ad Acr ss a Plurality f Pr c ss rs

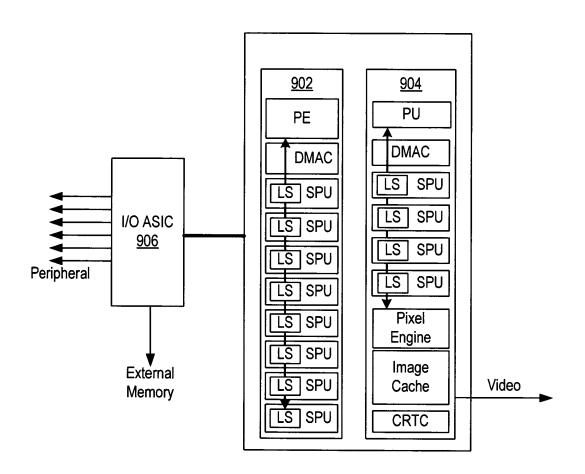


Figure 9

Docket No. AUS920030790US1

Minor, tal.

Syst m and Meth df r Balancing C mputati nal Load Acr ss a Plurality f Proc ssors

10/51

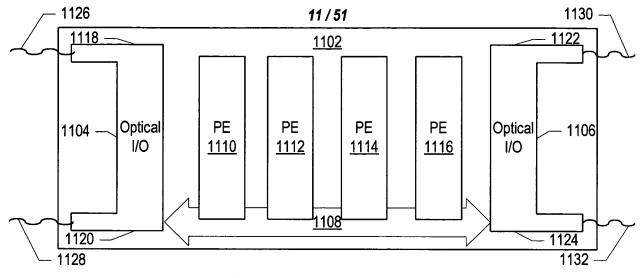
1002 PU **DMAC** LS SPU LS SPU I/O ASIC <u>1004</u> LS SPU Peripheral LS SPU Pixel **Engine** Image Video Cache CRTC

Figure 10

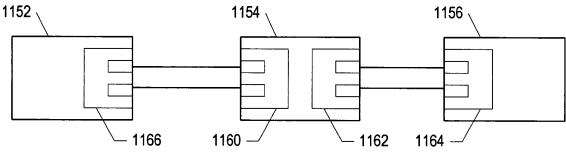
D cket No. AUS920030790US1

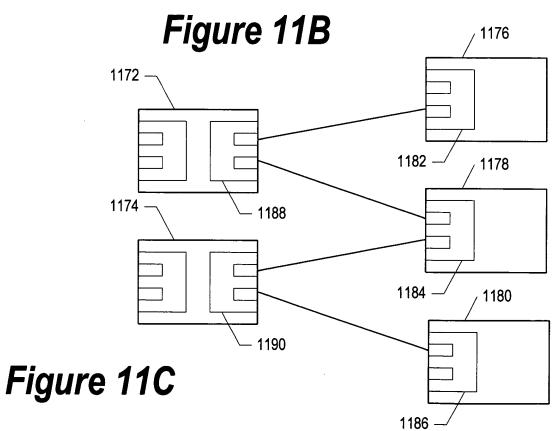
Min r, et al.

System and Meth df r Balancing C mputati nal L ad Acr ss a Plurality of Proc ssors





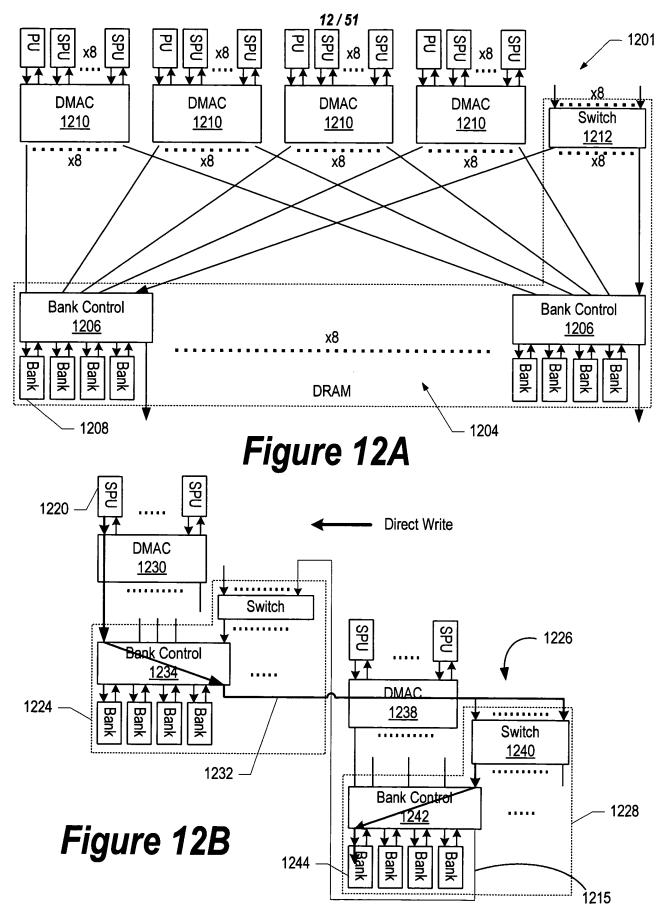




D ck t No. AUS920030790US1

Min r, t al.

Syst m and Meth d f r Balancing C mputati nal L ad Acr ss a Plurality f Pr c ss rs



D cket N . AUS920030790US1

Minor, et al.

System and Meth d for Balancing Computati nal L ad Acr ss a Plurality of Pr c ss rs

13/51

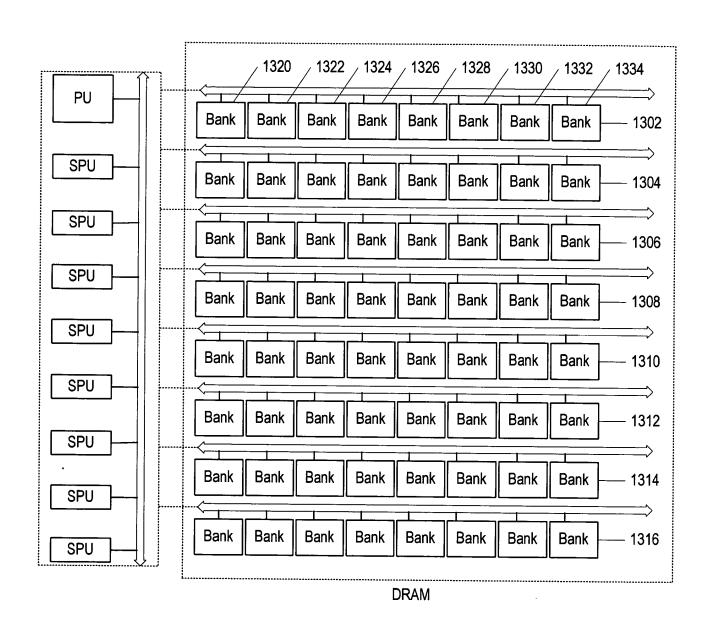


Figure 13

Dock t N . AUS920030790US1

Min r, t al.

Syst m and M th d f r Balancing C mputati nal L ad Acr ss a Plurality f Proc ssors

14/51

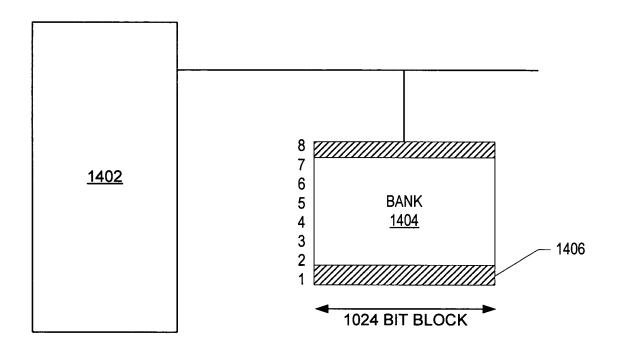


Figure 14A

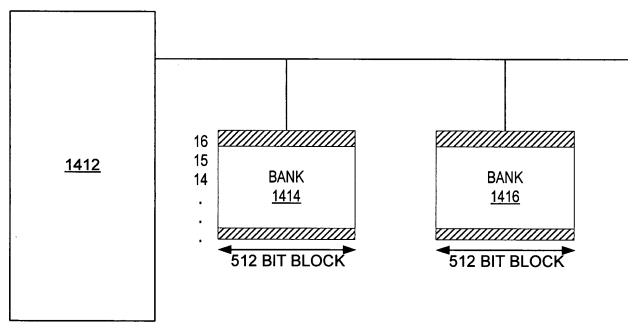


Figure 14B

D cket N . AUS920030790US1

Min r, et al.

System and M thod for Balancing C mputati nal L ad Acr ss a Plurality f Pr c ss rs

15/51

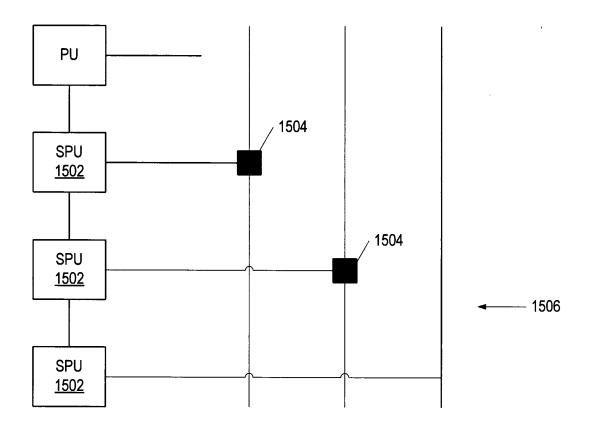


Figure 15

Dock t N . AUS920030790US1

Min r, et al.

Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality f Proc ss rs

16/51

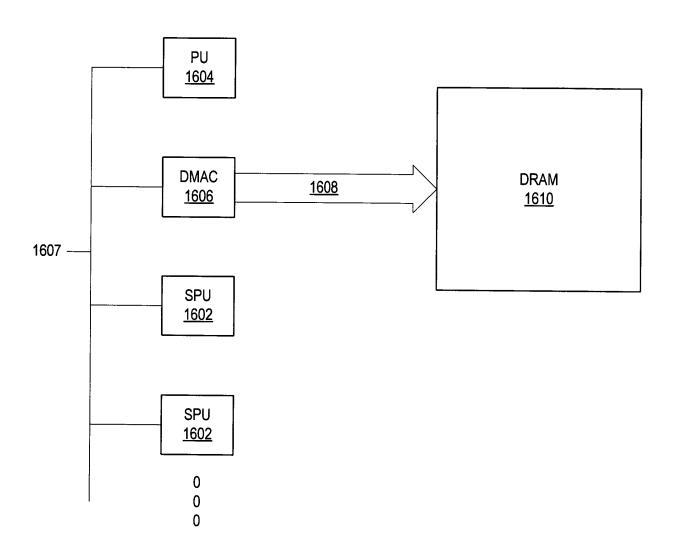
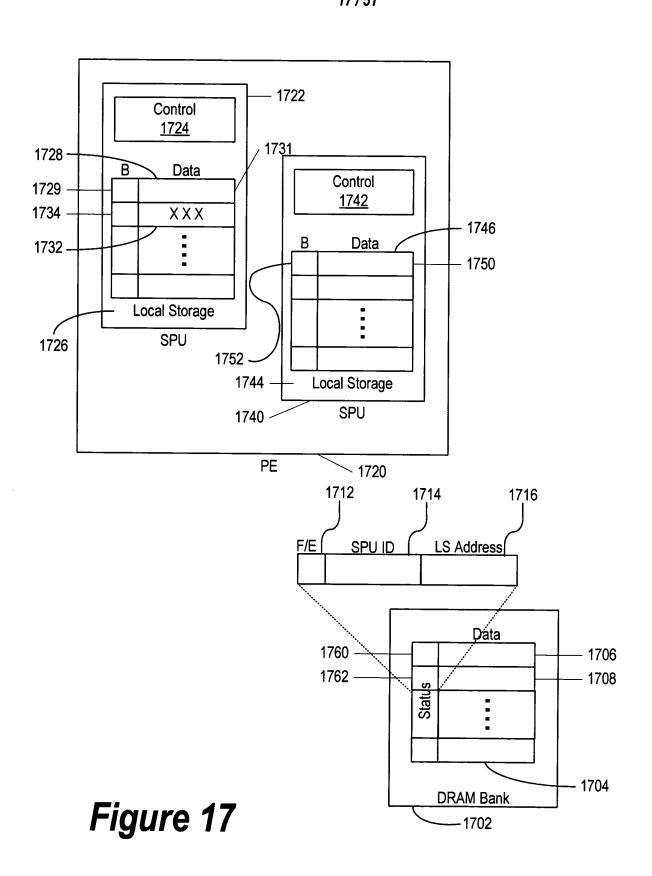


Figure 16

Docket No. AUS920030790US1

Min r, et al.

Syst m and M th d f r Balancing C mputati nal Load Acr ss a Plurality f Pr cess rs



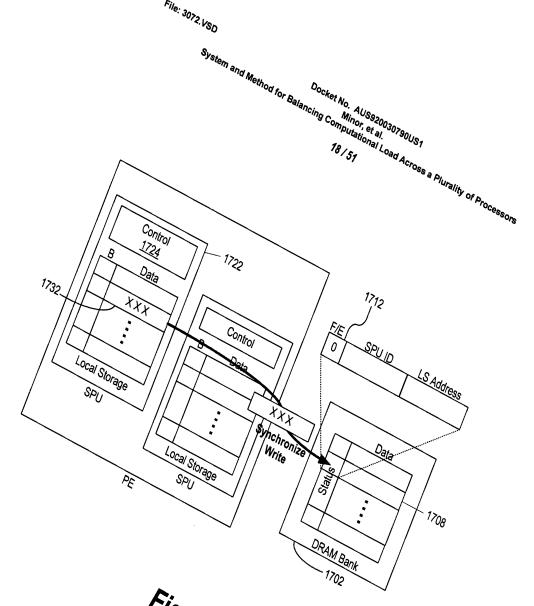


Figure 18

Docket N . AUS920030790US1

Min r, t al.

System and Meth df r Balancing C mputati nal L ad Acr ss a Plurality f Pr c ss rs

19/51

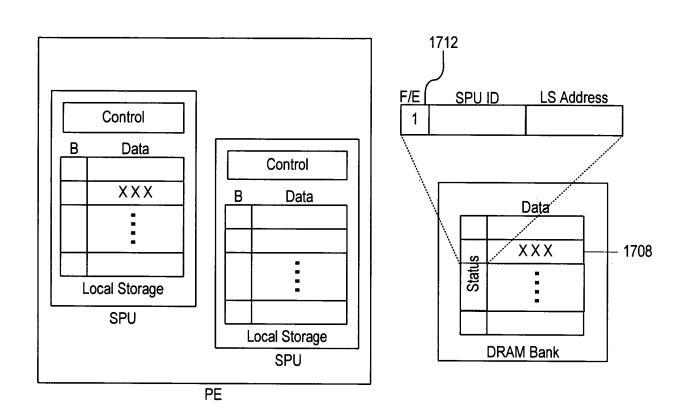
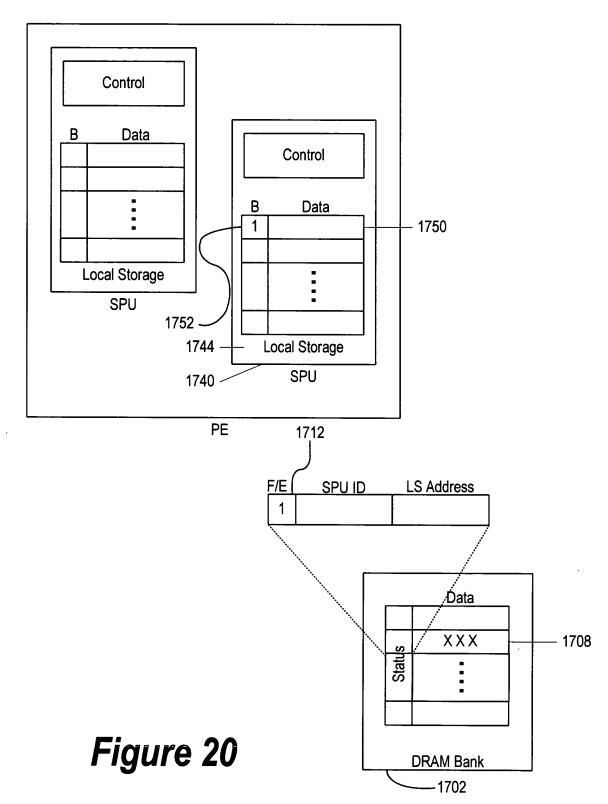


Figure 19

Dock t No. AUS920030790US1 Min r, et al. System and Meth d for Balancing C mputational L ad Across a Plurality f Pr cess rs



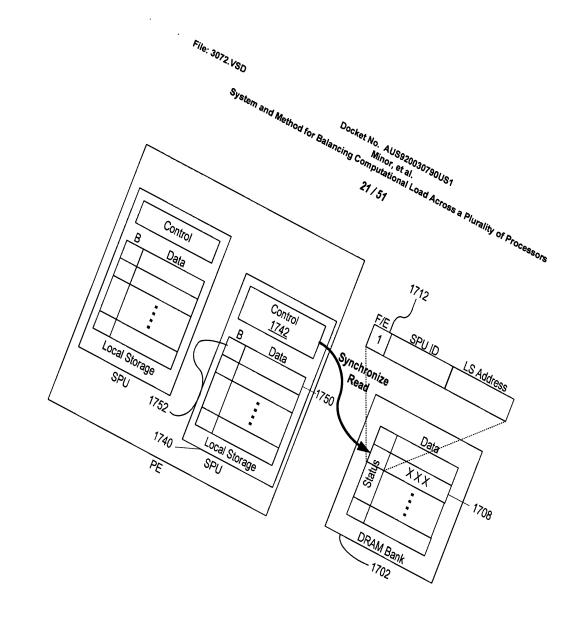
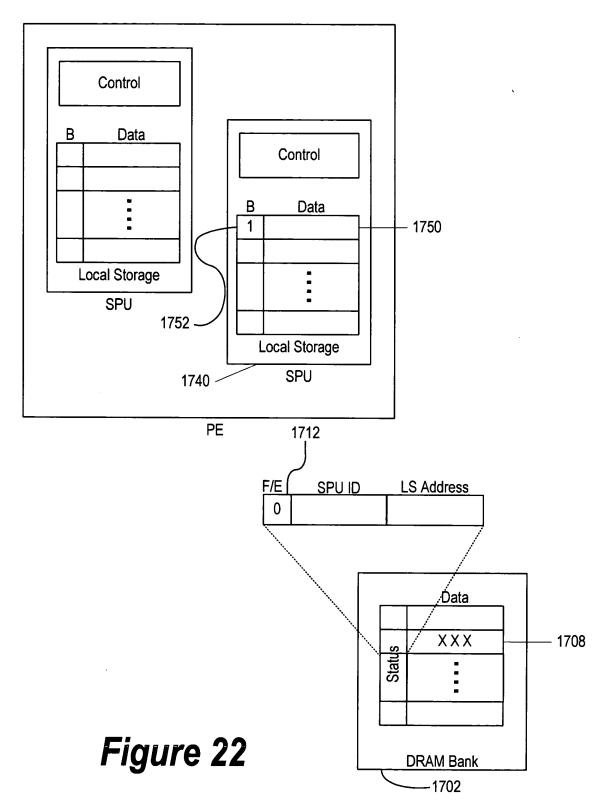


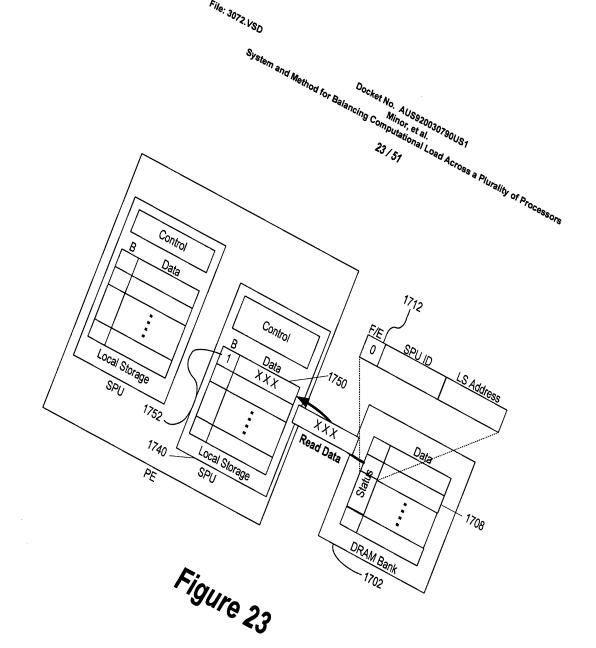
Figure 21

Dock t No. AUS920030790US1

Min r, et al.

System and Meth d f r Balancing C mputati nal L ad Across a Plurality of Pr c ss rs

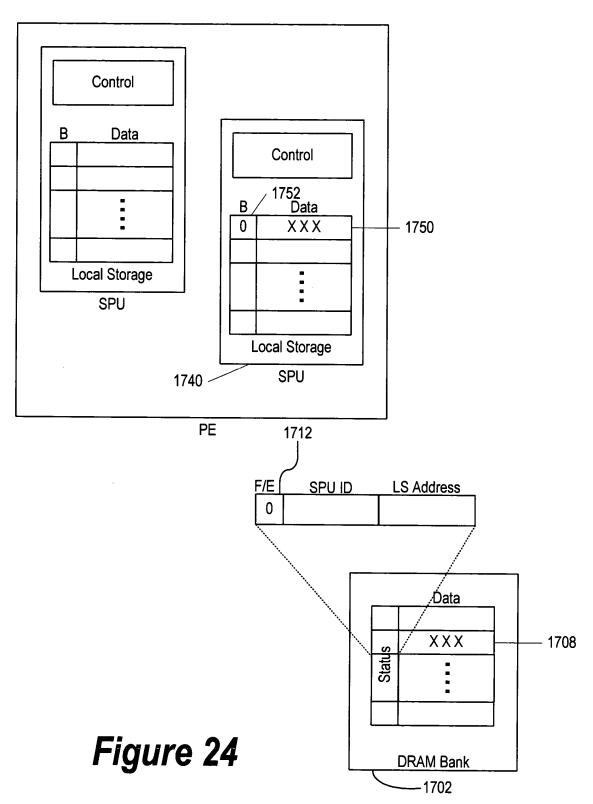




D cket No. AUS920030790US1

Min r, tal.

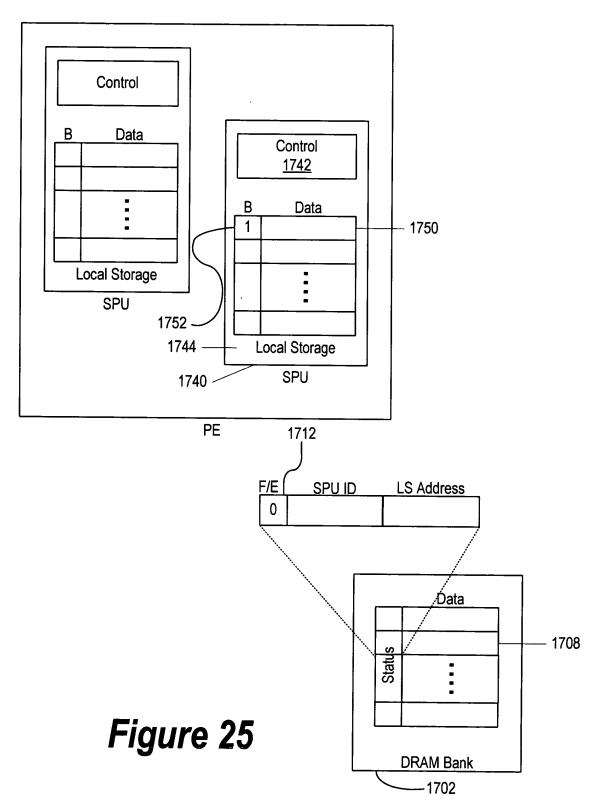
Syst m and Method f r Balancing C mputati nal L ad Acr ss a Plurality of Pr cess rs



Docket N . AUS920030790US1

Min r, et al.

Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality f Pr cessors



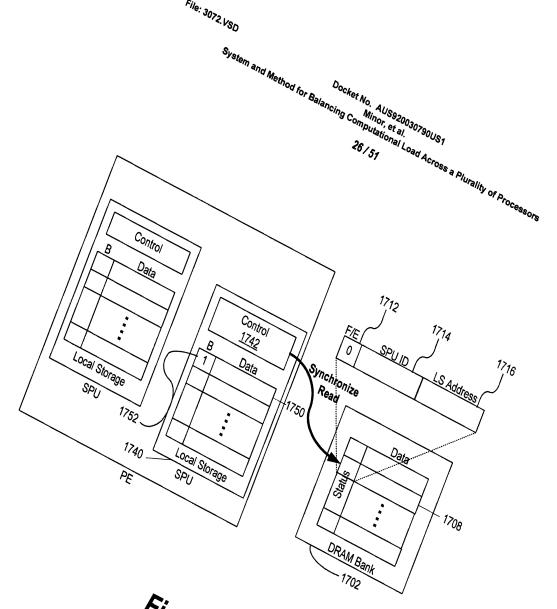


Figure 26

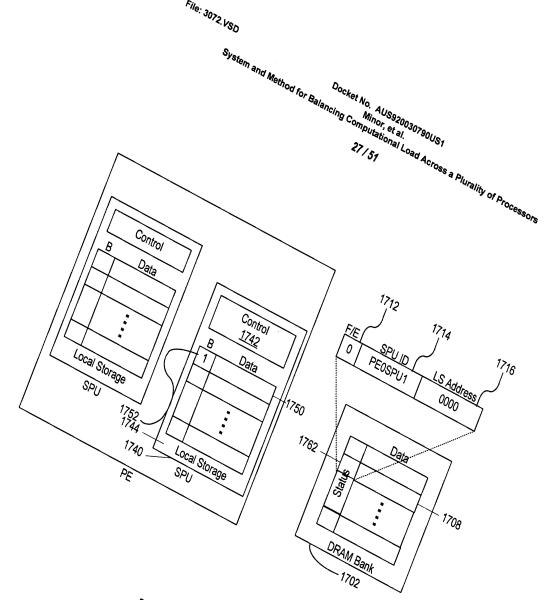


Figure 27

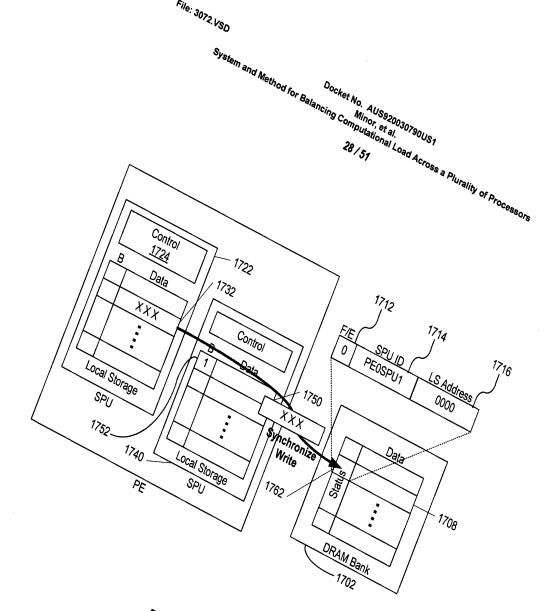


Figure 28

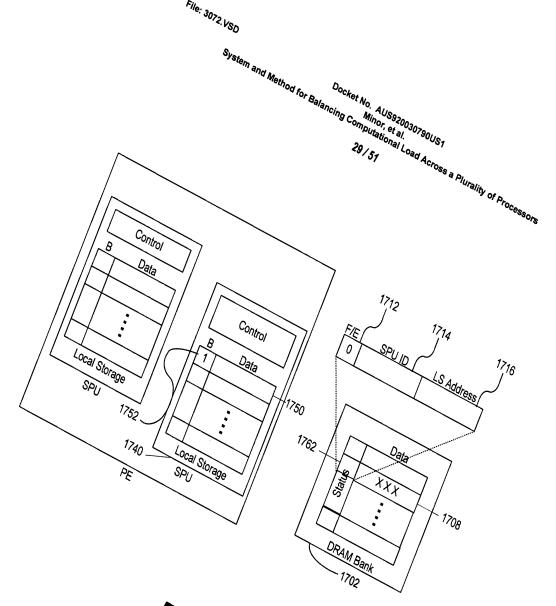


Figure 29

Dock t N . AUS920030790US1

Min r, et al.

System and Method f r Balancing C mputati nal Load Acr ss a Plurality f Pr c ss rs

30/51

1712 1714 1716 SPU ID LS Address Control 0 В Data Control 1750 Data Data XXXXXX- 1708 **Read Data** Local Storage SPU 1752 **Local Storage** DRAM Bank 1740 -SPU **— 1702** PE

Figure 30

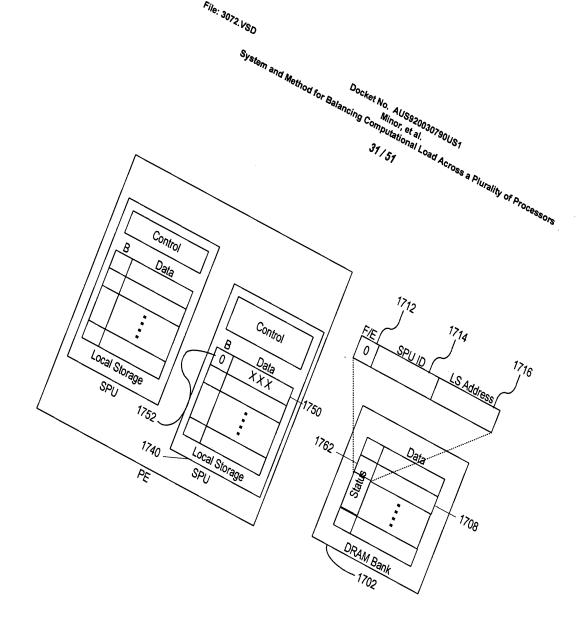


Figure 31

Docket No. AUS920030790US1

Min r, t al.

System and Meth d f r Balancing Computati nal L ad Acr ss a Plurality of Pr cess rs

32 / 51

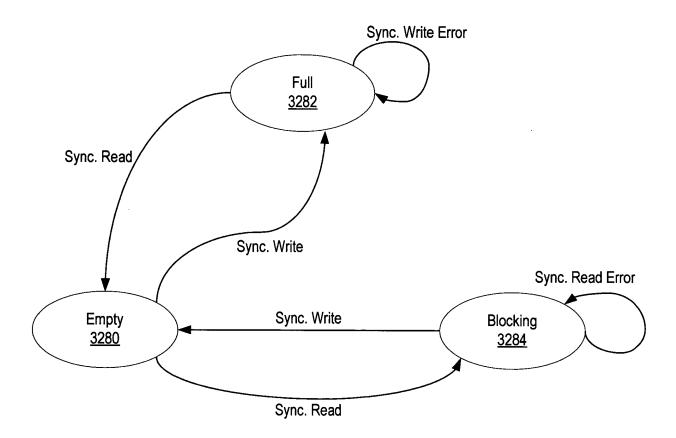


Figure 32

D cket No. AUS920030790US1 Min r, t al.

Syst m and Method f r Balancing C mputati nal L ad Acr ss a Plurality of Pr c ssors

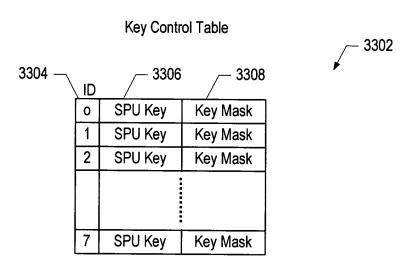


Figure 33

D cket N . AUS920030790US1

Minor, t al.

Syst m and M th d f r Balancing C mputati nal L ad Across a Plurality f Pr c ssors

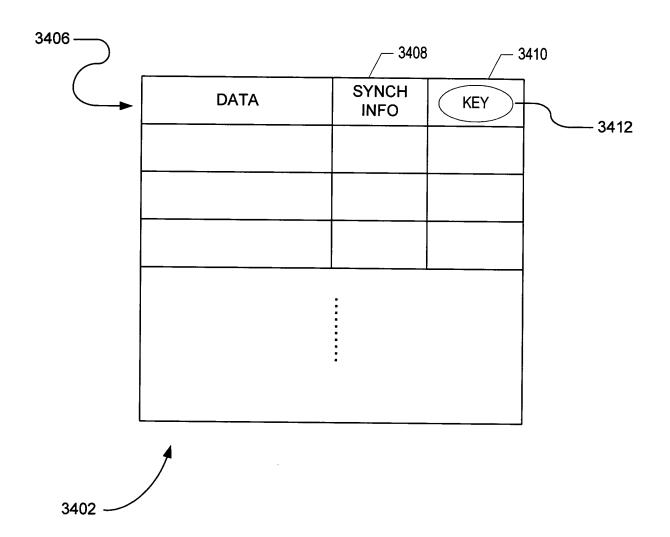


Figure 34

D cket No. AUS920030790US1 Min r, et al.

System and M thod f r Balancing C mputati nal L ad Acr ss a Plurality of Proc ss rs

35/51

Memory Access Control Table

3504 —	\ ID	— 3506		3508 3510	3512
	0	Base	Size	Access Key	Access Key Mask
	1	Base	Size	Access Key	Access Key Mask
	2	Base	Size	Access Key	Access Key Mask
	63	Base	Size	Access Key	Access Key Mask

3502

Figure 35

D ck t No. AUS920030790US1 Min r, et al.

Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality f Pr c ss rs

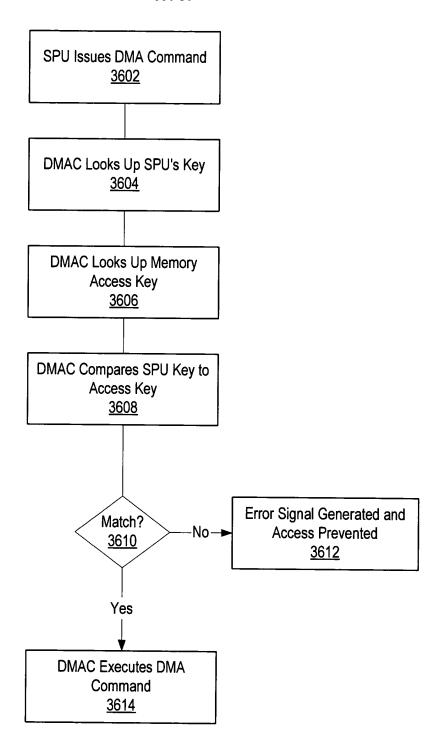


Figure 36

Docket No. AUS920030790US1

Min r, et al.

System and M thod f r Balancing C mputati nal L ad Acr ss a Plurality f Process rs

37/51

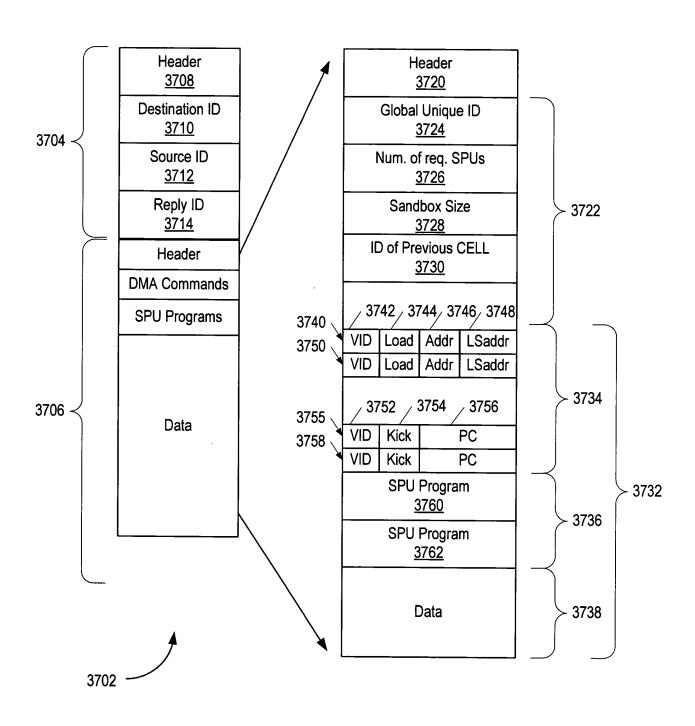


Figure 37

D cket No. AUS920030790US1

Minor, et al.

System and Meth d for Balancing C mputati nal Load Acr ss a Plurality f Pr c ssors

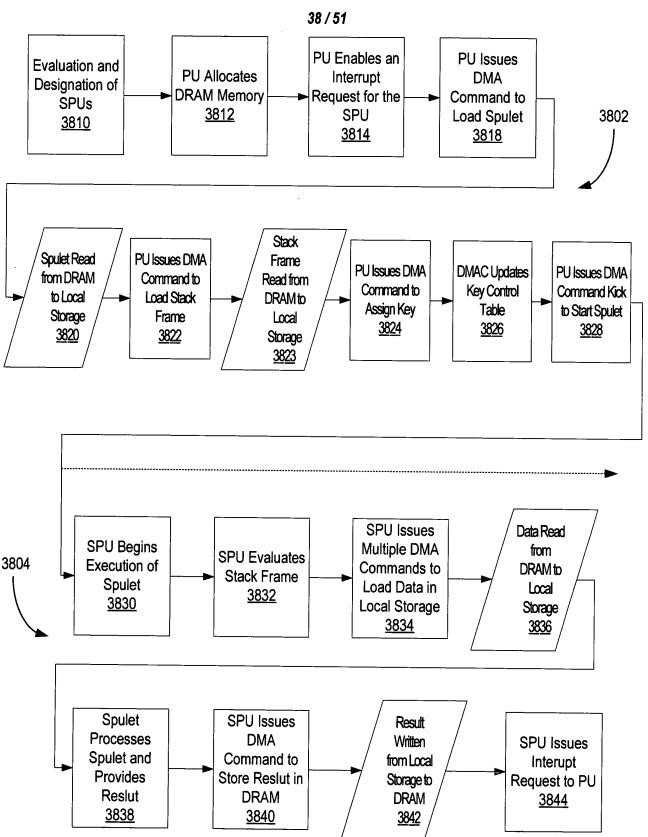


Figure 38

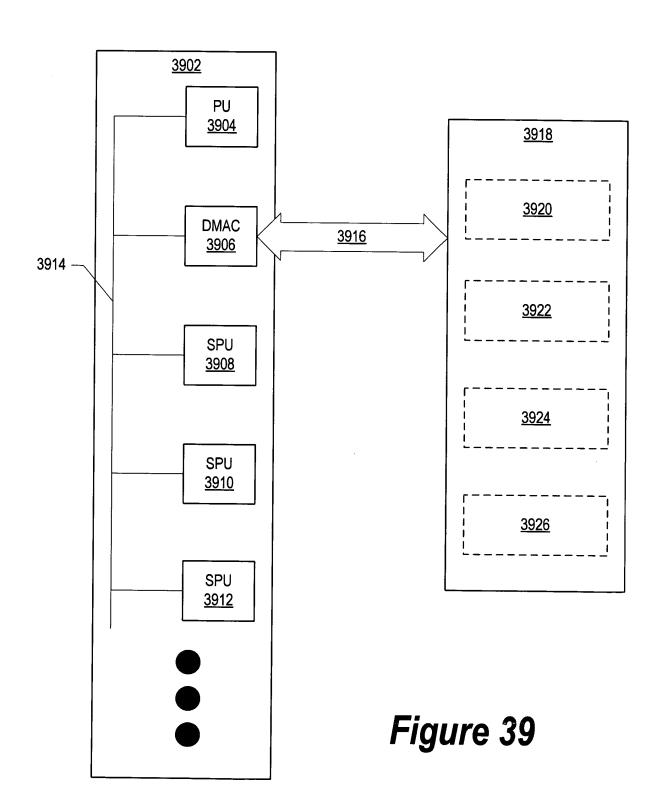
File: 3072.VSD

D ck t N . AUS920030790US1

Minor, et al.

System and Meth d for Balancing C mputati nal Load Acr ss a Plurality f Pr c ss rs

39/51



D ck t N . AUS920030790US1 Min r. et al.

System and Meth df r Balancing Computati nal L ad Acr ss a Plurality f Pr c ss rs

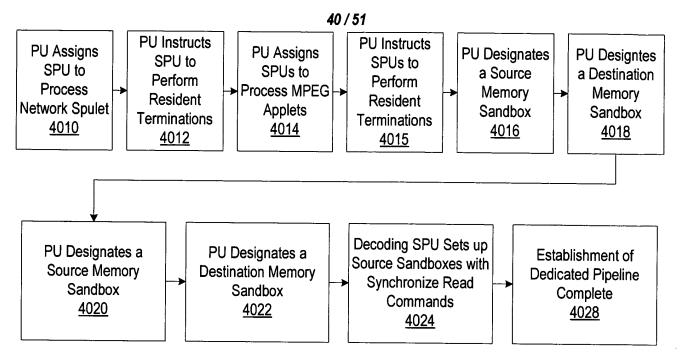


Figure 40A

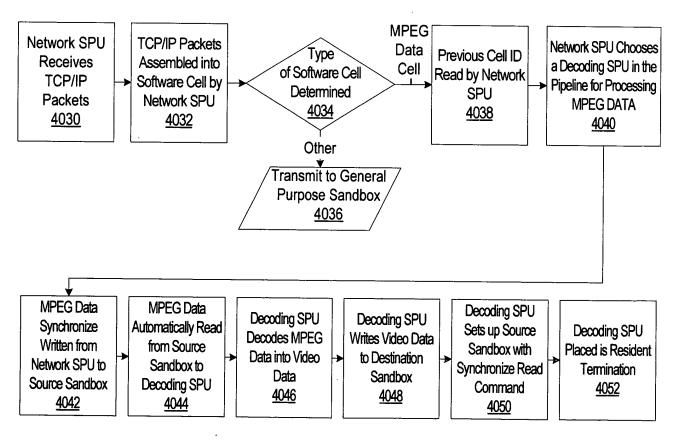


Figure 40B

Fil: 3072.VSD

Docket No. AUS920030790US1

Min r, tal.

Syst m and M th d f r Balancing Computational Load Acr ss a Plurality f Pr c ss rs

41 / 51

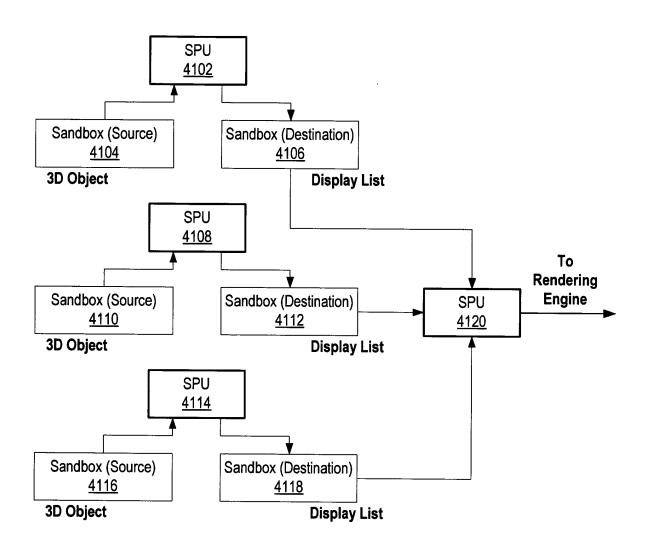


Figure 41

D ck t No. AUS920030790US1

Minor, t al.

System and Meth d f r Balancing C mputati nal L ad Across a Plurality f Pr c ss rs

42 / 51

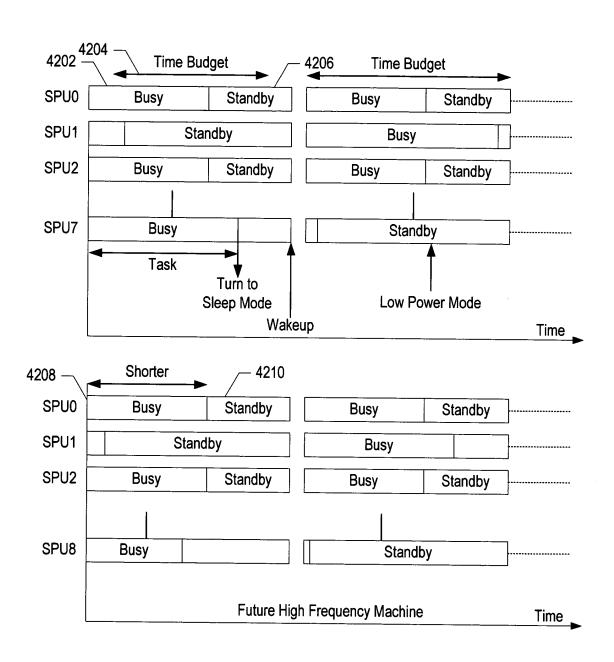
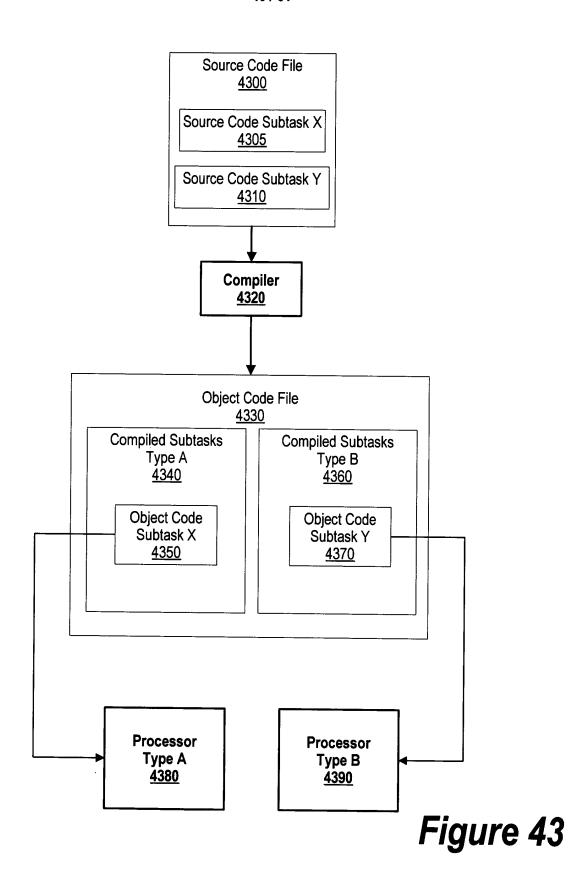


Figure 42

D cket No. AUS920030790US1 Min r, t al.

System and Method for Balancing C mputati nal Load Acr ss a Plurality f Pr cessors

43 / 51



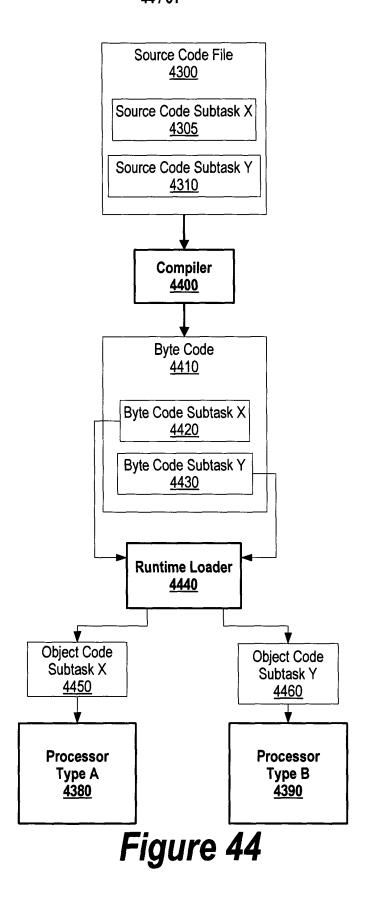
Fil: 3072.VSD

Dock t No. AUS920030790US1

Min r, tal.

Syst m and Meth d for Balancing C mputati nal Load Acr ss a Plurality f Pr cess rs

44 / 51



File: 3072.VSD

D cket N . AUS920030790US1

Minor, et al.

System and Method for Balancing C mputati nal Load Acr ss a Plurality f Pr c ss rs

45/51

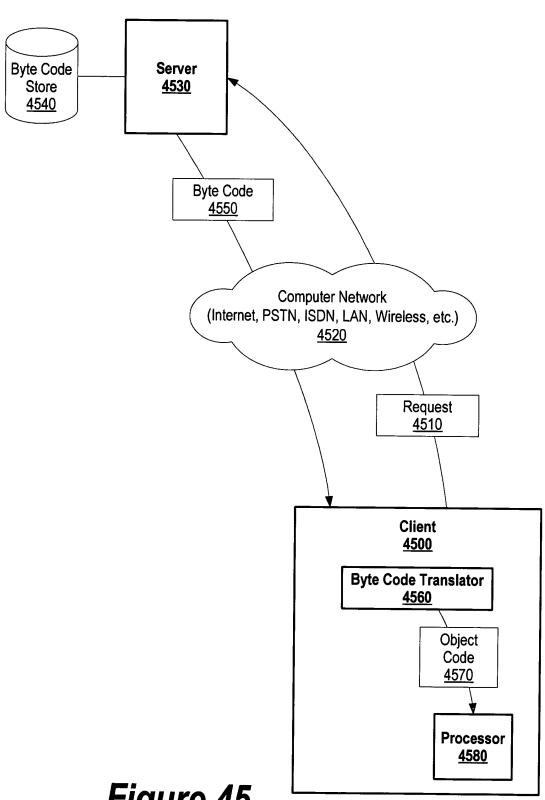


Figure 45

D cket No. AUS920030790US1

Min r, t al.

System and Method f r Balancing C mputati nal L ad Acr ss a Plurality f Pr cess rs

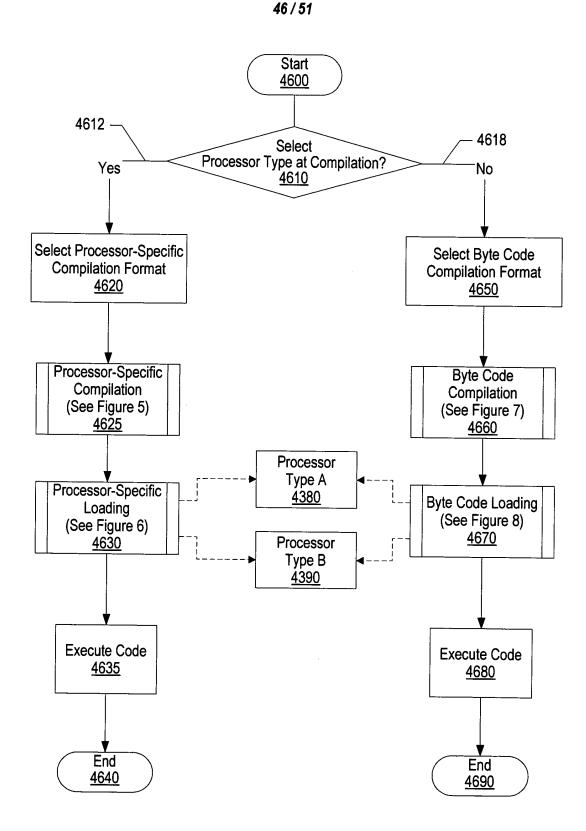
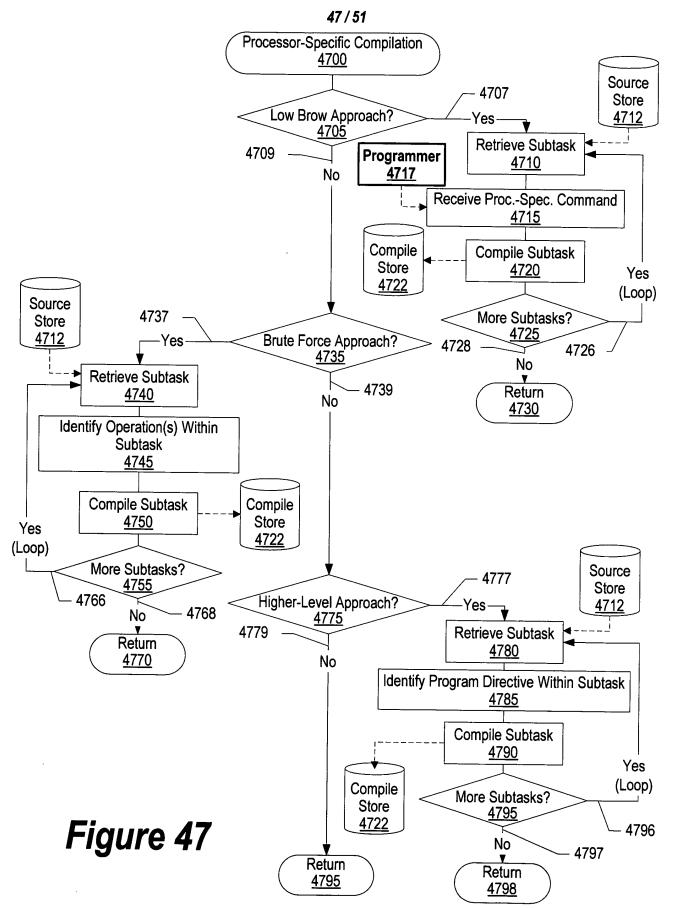


Figure 46

D cket No. AUS920030790US1

Min r, et al.

Syst m and M th d f r Balancing C mputati nal L ad Acr ss a Plurality of Proc ss rs



D ck t N . AUS920030790US1

Minor, et al.

System and Meth d for Balancing C mputati nal Load Acr ss a Plurality of Process rs

48 / 51

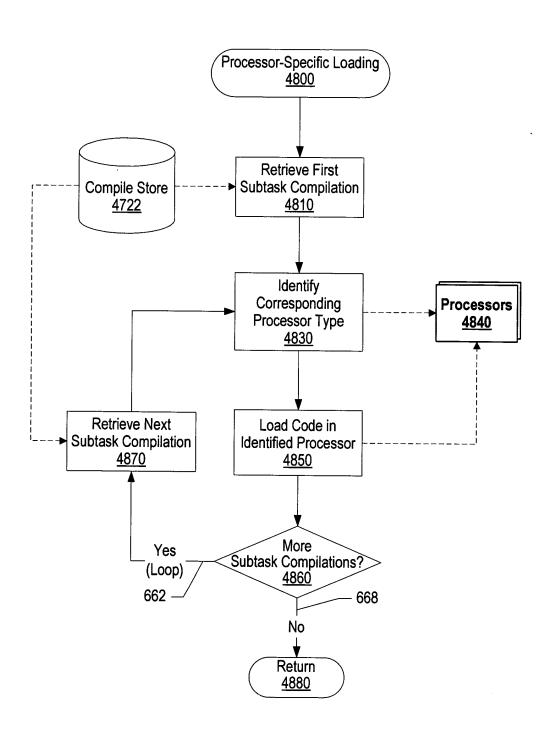


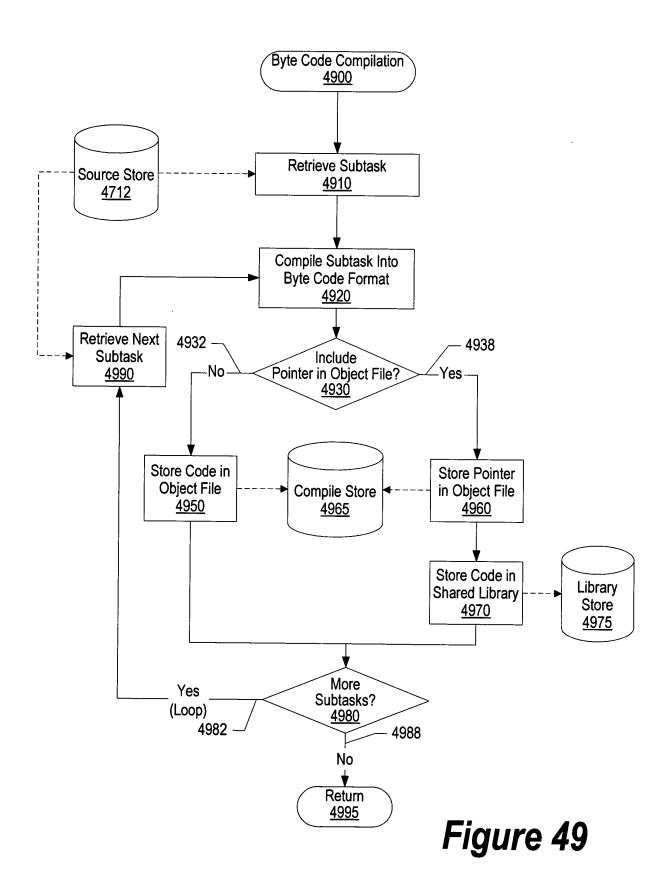
Figure 48

Docket No. AUS920030790US1

Min r, t al.

System and Meth d f r Balancing C mputati nal Load Acr ss a Plurality of Pr c ssors



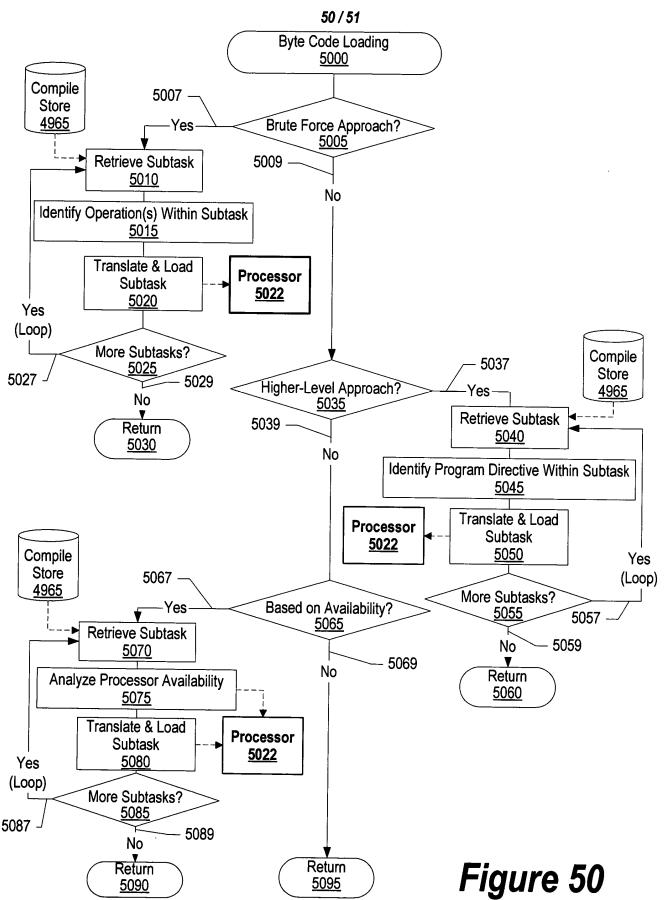


File: 3072.VSD

Dock t N . AUS920030790US1

Min r, et al.

Syst m and M th d for Balancing C mputati nal L ad Acr ss a Plurality of Proc ss rs



Docket No. AUS920030790US1 Minor, et al.

System and Method for Balancing Computational Load Across a Plurality of Processors

51/51

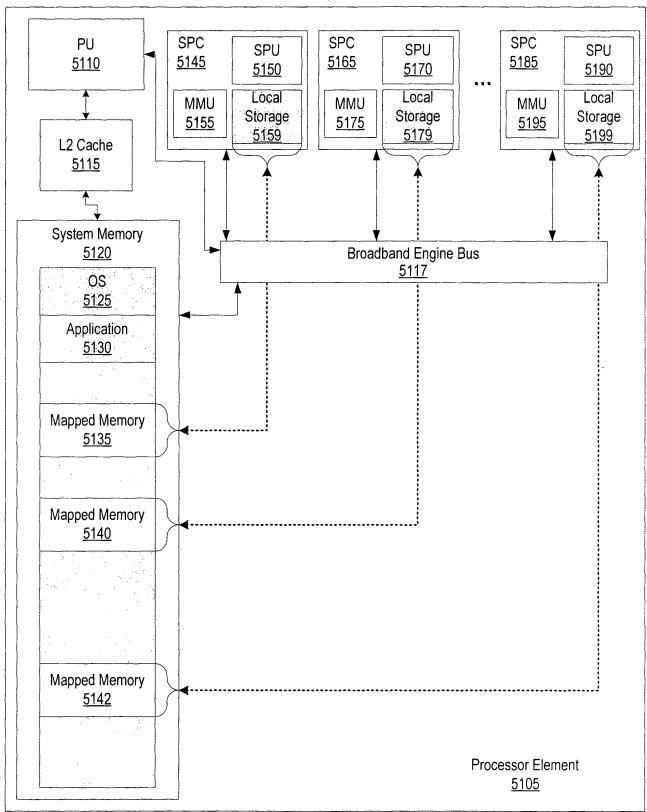


Figure 51